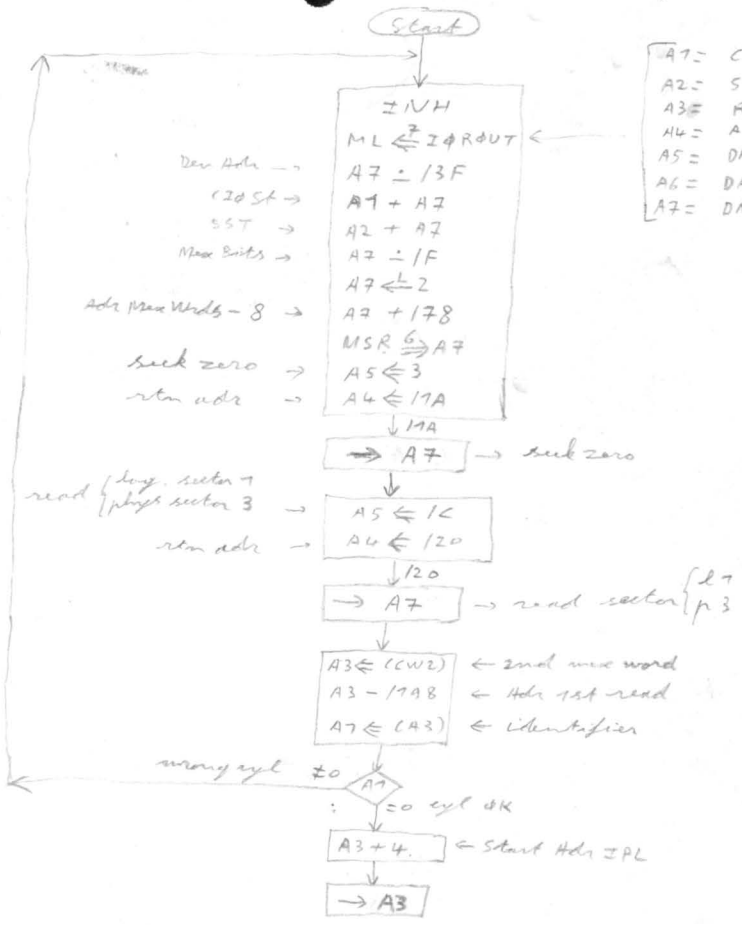


DKB00T
DOS/R2

A7 = CIB A5, 1, 0
 A2 = SST A5, 0
 A3 = RBCW * -2
 A4 = ABR A4
 A5 = DATA /4E66 } MIX WORDS
 A6 = DATA /3E00
 A7 = DATA /0002 DEV ADDR

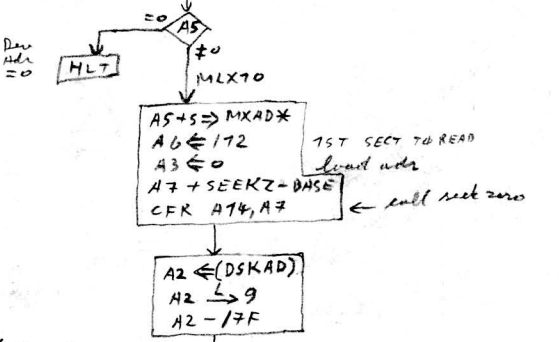
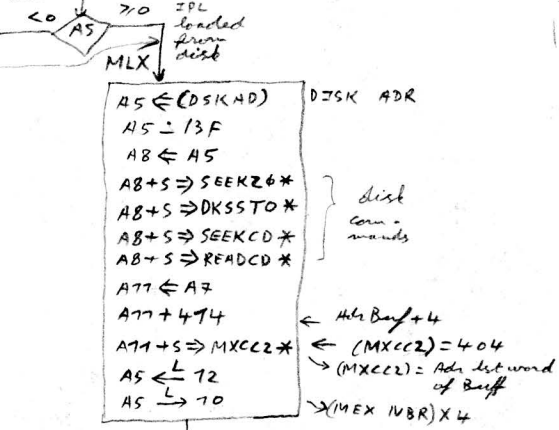
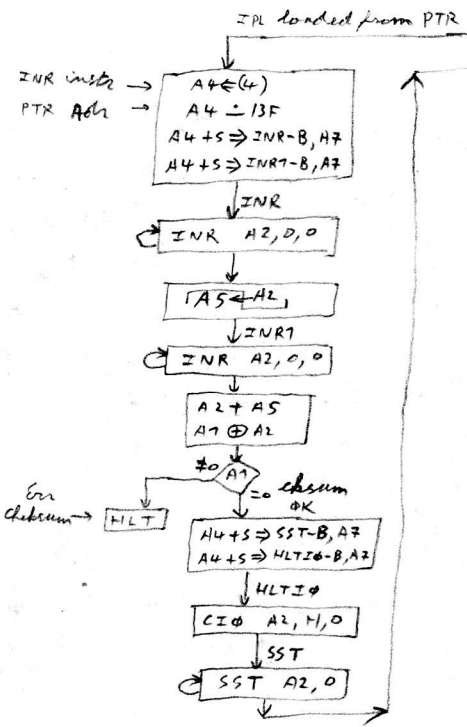


*X-BASE, A7

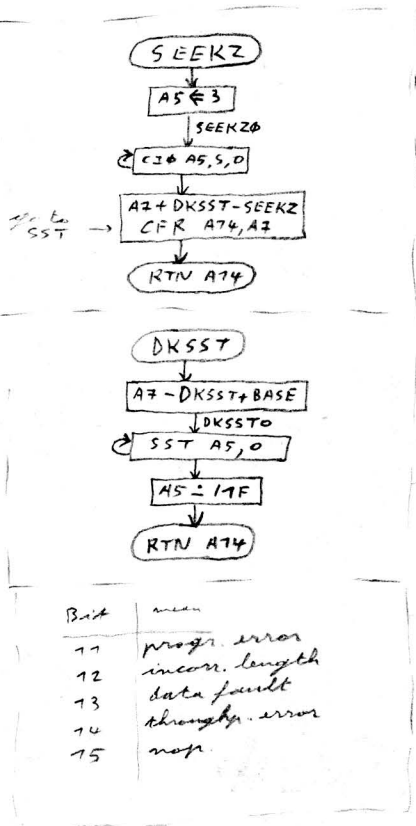
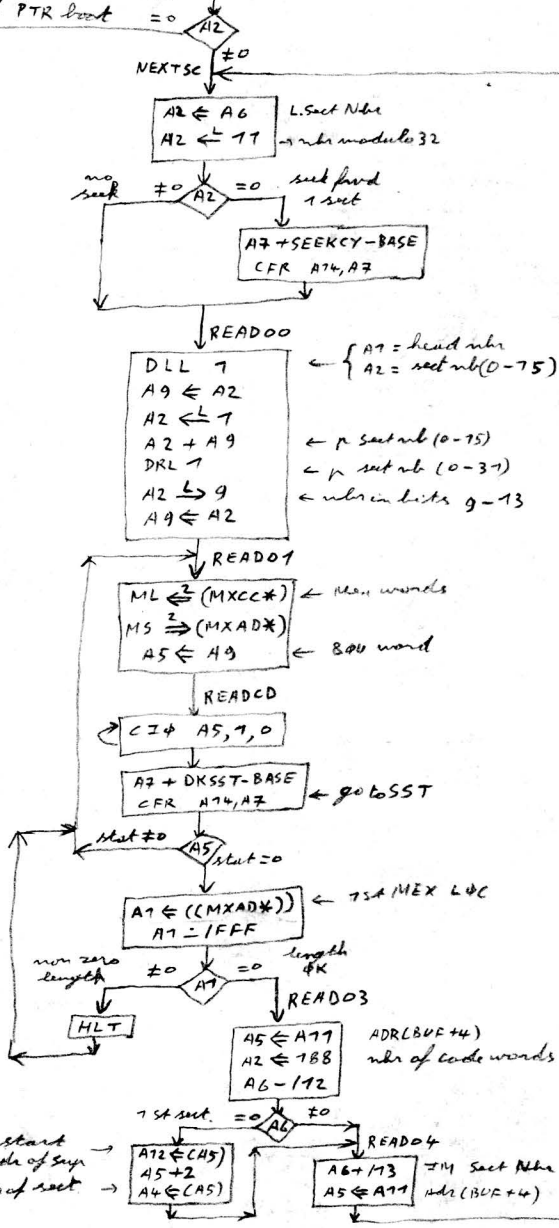
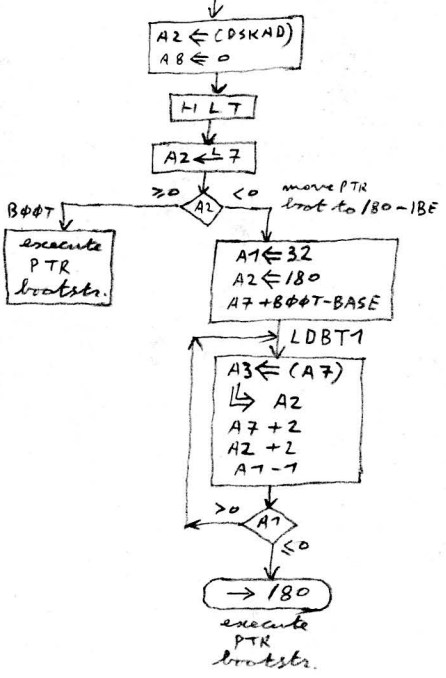
IPL DD

DATA 0 → cyl nbr
 DATA 400 → char count
 =PLOBASE

A74 ← A0 → A74 = HDR(BASE)
 A74 - 2 → A7 = HDR(BASE)
 IMH
 AS ← (DSKAD) → (loc 13E)
 AS ← 8

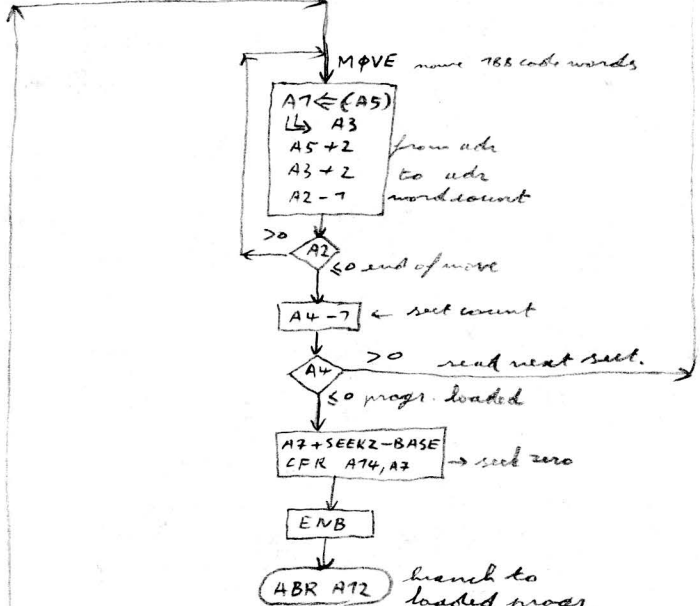
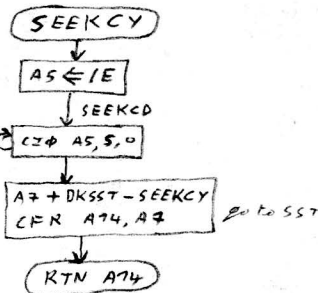


load PTR boot



Bit	mean
11	prog. error
12	incorr. length
13	data fault
14	through. error
15	noop

IPL DD



PREMRK

CHECK

```

A7 ← (W:SAV1)
A7 ← 3
A7 ← BITAB, A11
A6 ← 2.8
CF A14, ASKOUT
CF A14, SEEK0
A3 ← NBRCYL
  ↳ NBRTRK
  ↳ NBRSEC
  ↳ NBRGRN
  ↳ IDENT
A7 ← 0
  ↳ W:SAV1
  
```

LD BITA

A7 ← (BITAB, A11)

↳ W:SAV1+2

TST CYL

A7 ← NBRCYL

≠ (CYLMAX) = (CYLMAX)

END CHECK

A7 ← (NBRTRK)

≠ (TRKMAX) = (TRKMAX)

A7 ← (NBRSEC)

≠ (SECMAX) = (SECMAX)

A7 ← 7

↳ NBRTRK+7

↳ NBRSEC

↳ NBRTRK+7

↳ NBRTRK+7

↳ NBRTRK+7

↳ NBRTRK+7

↳ NBRTRK+7

↳ NBRTRK+7

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↳ NBRTRK+7

↳ NBRTRK+7

↳ NBRTRK+7

SSTVAL

SST A7, DSK

A7 ← 1/F

start $\phi K = 0$ A7 REWRITE

(NBRSEC)+7

A7 ← (NBRSEC)

A72 ← 7

≠ 0 A72 = 0

A7 ← (NBRSEC)

↳ NBRGRN+7

↳ NBRGRN+7

↳ NBRGRN+7

↳ NBRGRN+7

↳ NBRGRN+7

↳ NBRGRN+7

↳ NBRGRN+7

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↳ NBRGRN+7

↳ NBRGRN+7

A7

↳ 5 ⇒ CI ϕ 7

↳ 5 ⇒ READ

↳ 5 ⇒ CI ϕ 8

↳ 5 ⇒ CI ϕ 9

↳ 5 ⇒ CI ϕ 10

↳ 5 ⇒ I ϕ ER

A7 ← MESS72

A6 ← 8

CF A14, ASKOUT

A7 ← LABEL

A6 ← 8

CF A14, ASKIN

A7 ← MESS73

A6 ← 7

CF A14, ASKOUT

A7 ← DATE

A6 ← 8

CF A14, ASKIN

A7 ← MESS74

A6 ← 7

CF A14, ASKOUT

A7 ← PACNBR

A6 ← 3

CF A14, ASKIN

A7 ← (DUPAD)

A2 ← 1/F

A2 ← 2

A7 + 180

A7 ← MX ϕ VE

A73 ← W:SAV1

↳ W:SAV1+2

A71 ← 0

↳ NBRCYL

↳ NBRTRK

↳ NBRSEC

↳ NBRGRN

↳ NBDTRK

A7 ← MESS75

A6 ← 2.6

CF A14, ASKOUT

A5 ← 7

A4 ← 14987

↳ TST

A4 ← 14787

↳ CI ϕ HLT

A4 ← 14987

↳ SST2

PREMRK

A7 ← MESS0

A6 ← 3.2

I MH

A74 ← W:SAV1+70

CF A14, ASKOUT

A73 ← 0

MESS2

A7 ← MESS2

A6 ← 2.0

CF A14, ASKOUT

A7 ← 12020

↳ BUF

↳ BUF+2

A7 ← BUF

A6 ← 4

CF A14, ASKIN

A7 ← (BUF)

A2 ← (BUF+2)

A4 ← RET

I: DEBT

RET

R B(7)

A7 ← CYLMAX

MESS3

A7 ← MESS3

A6 ← 7.7

CF A14, ASKOUT

A7 ← 12020

↳ BUF

↳ BUF+2

A7 ← BUF

A6 ← 4

CF A14, ASKIN

A7 ← (BUF)

A2 ← (BUF+2)

A4 ← RET2

I: DEBT

RET2

R B(7)

A7 ← TRKMAX

MESS4

A7 ← MESS4

A6 ← 2.4

CF A14, ASKOUT

A7 ← 12020

↳ BUF

↳ BUF+2

A7 ← BUF

A6 ← 4

CF A14, ASKIN

A7 ← (BUF)

A2 ← (BUF+2)

A4 ← RET3

I: DEBT

RET3

R B(7)

A7 ← SEC MAX

MESS0

A7 ← MESS70

A6 ← 2.9

CF A14, ASKOUT

A7 ← 12020

↳ BUF

↳ BUF+2

A7 ← BUF

A6 ← 2

CF A14, ASKIN

A7 ← 13030

A2 ← (BUF)

A4 ← RET4

I: HXB

RET4

R B(2)

A7 ← DUPAD

↳ 5 ⇒ SEEK

↳ 5 ⇒ CI ϕ 2

↳ 5 ⇒ CI ϕ 2

↳ 5 ⇒ SEEK0

↳ 5 ⇒ CI ϕ 4

↳ 5 ⇒ CI ϕ 3

↳ 5 ⇒ VAL

↳ 5 ⇒ CI ϕ 33

↳ 5 ⇒ SSTVAL

↳ 5 ⇒ SST1

↳ 5 ⇒ CI ϕ SEK

↳ 5 ⇒ CI ϕ 6

↳ 5 ⇒ WRTE

'init premrk'

'mb of cyl'

'mb of tracks'

'mb sect / track'

'disk physics'

'label'

'date'

'track nbs'

'max nbs'

'writing the other options'

'checking the dependencies'

'not busy = 0

'= 3F all den. met'

'TSTRED

'TSTTRK CHECK

'TSTSEC

'INIA72

'WRTE

'CF A14, WRTE

'= 8, 76, 24, 32

'= 8, 76, 24, 32

'= 8, 76, 24, 32

'= 8, 76, 24, 32

'= 8, 76, 24, 32

'= 8, 76, 24, 32

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'= 8, 76, 24, 32

'= 8, 76, 24, 32

'= 8, 76, 24, 32

'= 8, 76, 24, 32

'= 8, 76, 24, 32

'CW76

'ZERTA

'ID

'WRTE

'CF A14, WRTE

'SST A7, DSK

'(NBRSEC)+7

'A72 ← 7

'= 0

'BITAL4

'ML ← (W:SAV1)

'A7 ← /FFFC

'MS ← W:SAV1

'(NBDTRK)+7

'(NBRGRN)+7

'DLL, 7

'MS ← W:SAV1

