

Unit 6 Exam

QuesNo Question

- 1 Device control CMs can be interlocked by process conditions. Which of the following is not a device control block interlock mechanism?
- A Safety override
 - B Process override
 - C Safety interlock option
 - D Process permissive
- 2 If a device control CM is active in the one-state and its configured OI[0] parameter goes true, what will take place?
- A The CM will go to its configured safe state and stay there until the OI[0] parameter goes false.
 - B The CM will stay in the one-state and not be allowed to go to the zero-state.
 - C The CM will go to the zero-state and stay there until the OI[0] parameter goes false.
 - D The CM will stay in the one-state, but not be allowed to return to the one-state after it goes to the zero-state until the OI[0] parameter goes false.
- 3 If a device control CM is active in the one-state and its configured PI[1] parameter goes false, what will take place?
- A The CM will go to its configured safe state and stay there until the PI[1] parameter goes true.
 - B The CM will stay in the one-state and not be allowed to go to the zero-state.
 - C The CM will go to the zero-state and stay there until the PI[1] parameter goes true.
 - D The CM will stay in the one-state, but not be allowed to return to the one-state after it goes to the zero-state until the PI[1] parameter goes true.

4 If a device control CM is active in the one-state and its configured SI parameter goes true, what will take place?

A The CM will go to its configured safe state and stay there until the SI parameter goes false.

B The CM will stay in the one-state and not be allowed to go to the zero-state.

C The CM will go to the zero-state and stay there until the SI parameter goes false.

D The CM will stay in the one-state, but not be allowed to return to the one-state after it goes to the zero-state until the SI parameter goes false.

5 One major difference between a safety override and a process override is that a process override can be by-passed while a safety override cannot.

A True

B False

6 What is the interlock mechanism for a regulatory control block?

A Safety override

B Process override

C Safety interlock option enabled with a safe OP configured

D Process permissive